

WHAT IS CLAIMED IS:

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1. An information-processing device that executes a specific process more frequently than other processes among a variety of processes, said information-processing device comprising:

10 a first processor capable of executing an instruction set corresponding to the variety of processes; and

a second processor capable of executing a portion of or the entire instruction set, said
15 second processor being capable of executing a part of said instruction set corresponding to the specific process more efficiently than said first processor,

20 wherein said second processor executes the specific process whereas said first processor executes the other processes.

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2. The information-processing device as claimed in claim 1, wherein all the processes are allocated to said second processor initially, wherein said second processor passes a given process
30 to said first processor by interrupting said first processor in a case in which an instruction other than the part of the instruction set corresponding to the specific process must be executed.

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3. The information-processing device as claimed in claim 1, wherein all the processes are allocated to said second processor initially, wherein said second processor passes a given process to said first processor by interrupting said first processor when an instruction that cannot be executed or cannot be efficiently executed by said second processor appears in said given process.

4. The information-processing device as claimed in claim 3, wherein said instruction that cannot be executed or cannot be efficiently executed by said second processor is a floating-point arithmetic operation.

5. The information-processing device as claimed in claim 1, wherein said second processor is capable of executing the part of said instruction set corresponding to the specific process more efficiently than said first processor by executing said specific process in parallel by use of at least one of a multi-threading method and a multi-processing method.

6. The information-processing device as claimed in claim 1, wherein said first processor is a general-purpose processor, wherein said second processor is a transaction processor designed to

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efficiently execute a transaction process as the specific process.

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7. The information-processing device as claimed in claim 1, wherein said first processor and said second processor share a memory space.

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8. The information-processing device as claimed in claim 1, wherein said information-processing device includes a plurality of first processors and second processors.

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9. An information-processing device that executes a specific process more frequently than other processes among a variety of processes, said information-processing device comprising:

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a first processor capable of executing an instruction set and designed to execute variety of processes; and

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a second processor capable of executing a portion of or the entire instruction set, said second processor being capable of executing multiples of specific processes concurrently and achieve efficient execution than said first processor,

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wherein said second processor executes the specific process whereas said first processor executes the other processes.

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10. The information-processing device as claimed in claim 9, wherein all the processes are allocated to said second processor initially, wherein said second processor passes a given process to said first processor in a case in which an instruction other than the part of the instruction set corresponding to the specific process must be executed.

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11. The information-processing device as claimed in claim 9, wherein all the processes are allocated to said second processor initially, wherein said second processor passes a given process to said first processor when an instruction that cannot be executed appears or the execution of the process is judged not by said second processor in said given process.

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12. The information-processing device as claimed in claim 11, wherein said instruction that cannot be executed or cannot be efficiently executed by said second processor is a floating-point arithmetic operation.

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13. The information-processing device as claimed in claim 9, wherein said second processor is capable of executing all or the part of said instruction set corresponding to the specific

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process more efficiently than said first processor
by executing said specific processes in parallel by
use of at least one of a multi-threading method and
a multi-processing method.

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14. The information-processing device as
10 claimed in claim 9, wherein said first processor is
a general-purpose processor, wherein said second
processor is a transaction processor designed to
efficiently execute a transaction process as the
specific process.

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15. The information-processing device as
20 claimed in claim 9, wherein said first processor and
said second processor share common memory address
space.

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16. The information-processing device as
claimed in claim 9, wherein said information-
processing device includes a plurality of first
30 processors and second processors.

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